

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L2	145	((predrive or (pre adj drive)) near3 (circuit or control\$4)) and amplifier	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/11/09 14:04
L3	48	((predrive or (pre adj drive)) near3 (circuit or control\$4)) and (input near3 amplifier) and (output near3 amplifier)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/11/09 14:04
L4	22	((predrive or (pre adj drive)) near3 (circuit or control\$4)) and amplifier and (level near3 shift\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/11/09 14:05
L5	12	((predrive or (pre adj drive)) near3 (circuit or control\$4)) and (input near3 amplifier) and (output near3 amplifier) and (level near3 shift\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/11/09 14:05
L7	338	((ic or (integrated adj2 circuit)) with (benefit or advantag\$4)) and (plasma adj2 display)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/11/09 14:43
L9	315	(ic or (integrated adj2 circuit)) with (benefit or advantag\$4) with (single adj2 chip)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/11/09 14:44